

ver2.0

Weather : cloudy , clear Date: 26/05/2015

Diagram illustrating memory stack structure:

- Stack boundary (vertical line).
- Memory slots (horizontal lines).
- Slot 2 (from top): Labeled **DARK**.
- Slot 3 (from top): Labeled **GB**.

5-8-62907, 9-6-28304, 14-2-48880
10-6-38597

ver2.0

Observers Present : ROBERT, HIRAO, ASAKURA

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Weather: clear → cloudy

Date: 25/05/2015

[illegible]

1-2-50647